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February 27, 2004

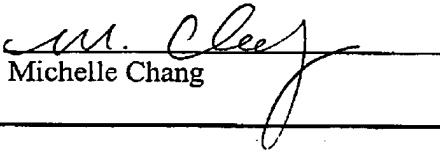
Atty Docket No. :	JCLA10931
Appl. No. :	10/612,607
Filing Date :	July, 01, 2003
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BY FACSIMILE ONLY

Fax No. :	703-872-9306
Attention :	EXAMINER : SOWARD, IDA M. Commissioner for Patents
Group Unit :	2822
From :	Jiawei Huang, Reg. No. 43,330
MESSAGE :	Enclosed herewith is a Response to Restriction Requirement in 1 page.

Sir:

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on February 27, 2004 at the above indicated fax number.

Sign by: 
Michelle Chang

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PATENT

FEB 27 2004

Docket No.: JCLA10931

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :

Applicant : YUN-SHENG CHEN et al.

Application No. : 10/612,607

Filed : July 01,2003

For : FABRICATING METHOD OF LOW TEMPERATURE POLY-SILICON FILM AND LOW TEMPERATURE POLY-SILICON THIN FILM TRANSISTOR

Art Unit : 2822

Examiner : SOWARD, IDA M.

RESPONSE TO RESTRICTION REQUIREMENT**MAIL STOP Non-Fee Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

In response to the Restriction Requirement mailed on February 12, 2004 regarding the above-identified application, Applicant elects Group I, claims 9 and 10, for further examination on merits. Please cancel claims 1-8 without prejudice and disclaimer.

No fee is believed to be due in connection with the filing of this paper. However, the Commissioner is authorized to charge any additional fees that may be required to Account No. 50-0710 (Order No. JCLA10931).

Respectfully submitted,
J.C. PATENTSDated: 2/27/2004By: Jiawei HuangJiawei Huang
Registration No. 43,330**Correspondence Address:**

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